

BOX FEE AMENDMENT  
COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

Applicant(s): Peter D. Geiger, Manual J. Alvarez II, Thomas A. Dye  
Assignee: Interactive Silicon, Inc.  
Title: System And Method For Generating Optimally Compressed Data From A  
Plurality Of Data Compression / Decompression Engines Implementing Different  
Data Compression Algorithms  
Serial No.: 10/044,785  
Filing Date: 1/11/02  
Docket No.: 40532-P001M11P3 (f/k/a 5143-03100)

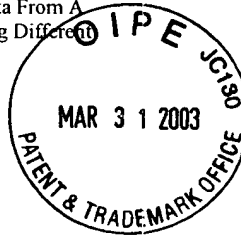
ENCLOSED:

- X This return receipt postcard
- 2 pages - Transmittal Letter (in duplicate)
- 25 pages - Response to Non-Final Office Action
- 12 pages - Revocation of Prior and Substitute Power of Attorney (which includes Exhibit A)
- X Winstead check in the amount of \$36.00

MPA/wk  
March 26, 2003

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March 26, 2003

BEST AVAILABLE COPY



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application(s) / Patent(s) of:

INTERACTIVE SILICON, INC.

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Dallas, Texas  
March 26, 2003

COMMISSIONER FOR PATENTS  
Washington, DC 20231

Dear Sir:

REVOCATION OF PRIOR AND SUBSTITUTE POWER OF ATTORNEY

I hereby revoke any and all prior Powers of Attorney executed in connection with the Patent Application(s) and/or Granted Patent(s) specified on the attached Exhibit A, and hereby appoint:

Michael P. Adams  
Reg. No. 34,763

Barry Newberger  
Reg. No. 41,527

my attorneys with full power of substitution and revocation, to prosecute the application(s) and patent(s) and to transact all business in the United States Patent and Trademark Office connected therewith, and to file and prosecute any international patent application(s) filed thereon before any international authorities under the Patent Cooperation Treaty.

Please refer to our file numbers specified on the attached Exhibit A.

Please send all correspondence to the address listed below:

Michael P. Adams  
Winstead Sechrest & Minick P.C.  
P.O. Box 50784  
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1201 Elm Street  
Dallas, Texas 75250-0784  
Telephone (512) 474-4330  
Facsimile (512) 370-2850

Date: 3-25-03

INTERACTIVE SILICON, INC.

By: Kenneth East

Printed: Kenneth East

Title: President & Chief Executive Officer

**EXHIBIT A**

- |    |                        |  |
|----|------------------------|--|
| 1. | Serial No:             | 08/340,667   |
|    | Filing Date:           | 11/16/94   |
|    | Patent No.:            | 6,002,411  |
|    | Issue Date:            | 12/14/99   |
|    | Title:                 | INTEGRATED VIDEO AND MEMORY<br>CONTROLLER WITH DATA PROCESSING<br>AND GRAPHICAL PROCESSING<br>CAPABILITIES |
|    | Assignment Reel/Frame: | 9275/0387  |
|    | Our File No.:          | 40532-P001X1   |
|    | Former File No.:       | 5143-00101   |
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- |    |                        |  |
|----|------------------------|--|
| 2. | Serial No:             | 09/241,139   |
|    | Filing Date:           | 2/1/99   |
|    | Patent No.:            | 6,370,631  |
|    | Issue Date:            | 4/9/02   |
|    | Title:                 | MEMORY CONTROLLER INCLUDING<br>EMBEDDED COMPRESSION /<br>DECOMPRESSION CAPABILITIES FOR<br>IMPROVED DATA ACCESS BETWEEN NON-<br>VOLATILE AND SYSTEM MEMORY |
|    | Assignment Reel/Frame: | 9758/0602  |
|    | Our File No.:          | 40532-P001D2   |
|    | Former File No.:       | 5143-00202   |
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3.	Serial No:	09/461,172
	Filing Date:	12/14/99
	Patent No.:	6,170,047
	Issue Date:	1/2/01
	Title:	SYSTEM AND METHOD FOR MANAGING SYSTEM MEMORY AND/OR NON- VOLATILE MEMORY USING A MEMORY CONTROLLER WITH INTEGRATED COMPRESSION AND DECOMPRESSION CAPABILITIES
	Our File No.:	40532-P001D3
	Former File No.:	5143-00203

4.	Serial No:	08/916,464
	Filing Date:	8/8/97
	Patent No.:	6,173,381
	Issue Date:	1/9/01
	Title:	MEMORY CONTROLLER INCLUDING EMBEDDED DATA COMPRESSION AND DECOMPRESSION ENGINES
	Our File No.:	40532-P001D1X2
	Former File No.:	5143-00204

5.	Serial No:	08/565,103
	Filing Date:	11/30/95
	Patent No.:	5,838,334
	Issue Date:	11/17/98
	Title:	MEMORY AND GRAPHICS CONTROLLER WHICH PERFORMS POINTER-BASED DISPLAY LIST VIDEO REFRESH OPERATIONS
	Assignment Reel/Frame:	9275/0371
	Our File No.:	40532-P001P1C1
	Former File No.:	5143-00301

6.	Serial No:	08/604,670
	Filing Date:	2/21/96
	Patent No.:	5,995,120
	Issue Date:	11/30/99
	Title:	GRAPHICS SYSTEM INCLUDING A VIRTUAL FRAME BUFFER WHICH STORES VIDEO/PIXEL DATA IN A PLURALITY OF MEMORY AREAS
	Our File No.:	40532-P001M1X1
	Former File No.:	5143-00304

7.	Serial No:	08/770,017
	Filing Date:	12/19/96
	Patent No.:	6,108,014
	Issue Date:	8/22/00
	Title:	SYSTEM AND METHOD FOR SIMULTANEOUSLY DISPLAYING A PLURALITY OF VIDEO DATA OBJECTS HAVING DIFFERENT BIT PER PIXEL FORMATS
	Our File No.:	40532-P001M2X1
	Former File No.:	5143-00305

8.	Serial No:	09/971,107
	Filing Date:	10/4/01
	Patent No.:	6,518,965 B2
	Issue Date:	2/11/03
	Title:	GRAPHICS SYSTEM AND METHOD FOR RENDERING INDEPENDENT 2D AND 3D OBJECTS USING POINTER BASED DISPLAY LIST VIDEO REFRESH OPERATIONS
	Assignment Reel/Frame:	012505/0682
	Our File No.:	40532-P021C1
	Former File No.:	5143-01103

9.        Serial No:                                09/056,021  
          Filing Date:                            4/6/98  
          Patent No.:                            6,067,098  
          Issue Date:                            5/23/00  
          Title:                                   VIDEO/GRAPHICS CONTROLLER WHICH  
   PERFORMS POINTER-BASED DISPLAY LIST  
   VIDEO REFRESH OPERATIONS  
  
          Assignment Reel/Frame:               9274/0804  
          Our File No.:                           40532-P001M3P1  
          Former File No.:                       5143-01200

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10.       Serial No:                               10/085,274  
          Filing Date:                            2/28/02  
          Title:                                   VIDEO CONTROLLER SYSTEM WITH  
   SCREEN CACHING  
  
          Assignment Reel/Frame:               012663/0652  
          Our File No.:                           40532-P003C1  
          Former File No.:                       5143-01501

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11.       Serial No:                               09/239,659  
          Filing Date:                            1/29/99  
          Title:                                   BANDWIDTH REDUCING MEMORY  
   CONTROLLER INCLUDING SCALABLE  
   EMBEDDED PARALLEL DATA  
   COMPRESSION AND DECOMPRESSION  
   ENGINES  
  
   **\*\*\*Response to Office Action submitted on  
8/5/02 requested title change to: SELECTIVE  
LOSSLESS, LOSSY, OR NO COMPRESSION  
OF DATA BASED ON ADDRESS RANGE,  
DATA TYPE, AND/OR REQUESTING AGENT**  
  
          Assignment Reel/Frame:               9742/0780  
          Our File No.:                           40532-P001M4P1  
          Former File No.:                       5143-01700

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15.	Serial No:	09/821,785
	Filing Date:	3/28/01
	Title:	SYSTEM AND METHOD FOR PERFORMING SCALABLE EMBEDDED PARALLEL DATA DECOMPRESSION
	Assignment Reel/Frame:	011954/0206
	Our File No.:	40532-P001M8P2
	Former File No.:	5143-01705

16.	Serial No:	09/616,480
	Filing Date:	7/14/00
	Title:	MEMORY MODULE INCLUDING SCALABLE EMBEDDED PARALLEL DATA COMPRESSION AND DECOMPRESSION ENGINES
	Assignment Reel/Frame:	011210/0355
	Our File No.:	40532-P022US
	Former File No.:	5143-01801

17.	Serial No:	09/299,966
	Filing Date:	4/26/99
	Patent No.:	6,145,069
	Issue Date:	11/7/00
	Title:	PARALLEL DECOMPRESSION AND COMPRESSION SYSTEM AND METHOD FOR IMPROVING STORAGE DENSITY AND ACCESS SPEED FOR NON-VOLATILE MEMORY AND EMBEDDED MEMORY DEVICES
	Assignment Reel/Frame:	9925/0590
	Our File No.:	40532-P001M9P3
	Former File No.:	5143-01900

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|-------|------------------------|--|
| 18.   | Serial No:             | 09/550,380   |
|       | Filing Date:           | 4/14/00  |
|       | Patent No.:            | 6,523,102 B1   |
|       | Issue Date:            | 2/18/03  |
|       | Title:                 | PARALLEL COMPRESSION /<br>DECOMPRESSION SYSTEM AND METHOD<br>FOR IMPLEMENTATION OF IN-MEMORY<br>COMPRESSED CACHE IMPROVING<br>STORAGE DENSITY AND ACCESS SPEED<br>FOR INDUSTRY STANDARD MEMORY<br>SUBSYSTEMS AND IN-LINE MEMORY<br>MODULES |
|       | Assignment Reel/Frame: | 010730/0424  |
|       | Our File No.:          | 40532-P007US   |
|       | Former File No.:       | 5143-02000   |
| <hr/> |                        |  |
| 19.   | Serial No:             | 09/840,724   |
|       | Filing Date:           | 4/23/01  |
|       | Title:                 | SYSTEM AND METHOD FOR RECOGNIZING<br>AND CONFIGURING DEVICES EMBEDDED<br>ON MEMORY MODULES   |
|       | Assignment Reel/Frame: | 011755/0793  |
|       | Our File No.:          | 40532-P008US   |
|       | Former File No.:       | 5143-02400   |
| <hr/> |                        |  |
| 20.   | Serial No:             | 09/915,751   |
|       | Filing Date:           | 7/26/01  |
|       | Title:                 | SYSTEM AND METHOD FOR MANAGING<br>COMPRESSION AND DECOMPRESSION OF<br>SYSTEM MEMORY IN A COMPUTER<br>SYSTEM  |
|       | Assignment Reel/Frame: | 012493/0726  |
|       | Our File No.:          | 40532-P009US   |
|       | Former File No.:       | 5143-02501   |
| <hr/> |                        |  |



24.	Serial No:	10/227,607
	Filing Date:	8/23/02
	Title:	MANAGING A CODEC ENGINE FOR MEMORY COMPRESSION / DECOMPRESSION OPERATIONS USING A DATA MOVEMENT ENGINE
	Our File No.:	40532-P014US
	Former File No.:	5143-03601

AUSTIN\_1\210308\1  
40532-1 03/24/2003